

Notice of References Cited	Application/Control No. 10/737,022	Applicant(s)/Patent Under Reexamination RUSU ET AL.	
	Examiner Maki A. Angadi	Art Unit 1765	Page 1 of 1

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*	B	US-2004/0221958 A1	11-2004	Loewenhardt et al.	156/345.47
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. Wolf, Silicon Processing for the VLSI Era, Lattice Press (1986), pages 575, 547, 549.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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